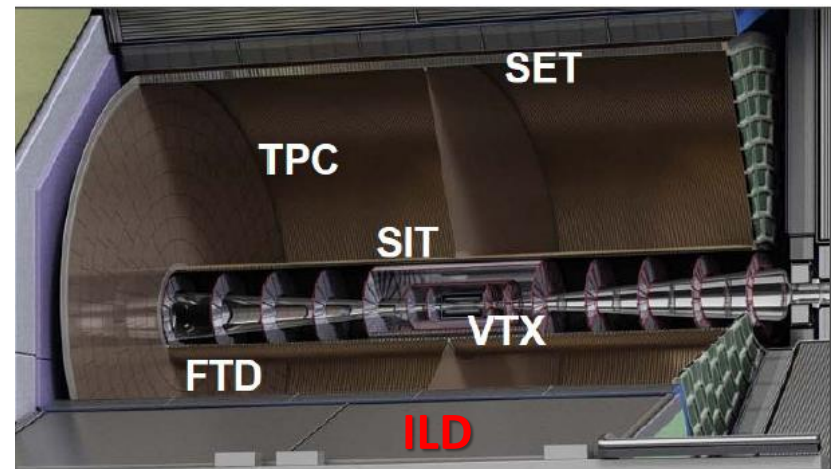


Interplay beam pipe, background and vertex detectors: Concerns on Beam Related Background Governing the Design of Tracking Devices

Marc Winter (IJCLAB - Orsay) & Auguste Besson (IPHC – Strasbourg)

- Impact of BS on technological choices of tracking sub-systems
- Questions on predictions
- Questions on beam optics and BS envelope
- Questions on BG besides BS
- Questions on beam pipe



Reminder: Impact of BS on technological choices

- Beam related BG dominates the hit rate of vertex detector as well as inner and end-cap trackers

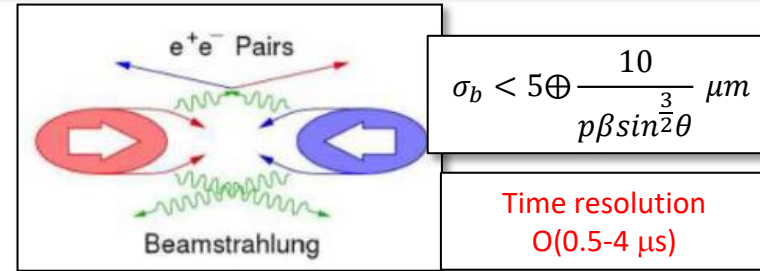
- ✓ It governs their read-out architectures & technological choices
- ✓ Percent level occupancy is considered as acceptable: ~few BX resolution time needed (0.5 - 4 μ s)

- For the vertex detector, it narrows down the sensor technological choice (presently) to CMOS pixel sensors, excluding for instance FPCCDs, which are more precise but not adapted to the hit rate:

- ✓ ~ 3 μ m (CMOS) against 1 μ m (FPCCD)

- Even in the case of CMOS pixel sensors, a trade-off is to be found to accommodate the hit rate, based on an interplay between pixel pitch, read-out speed and power consumption

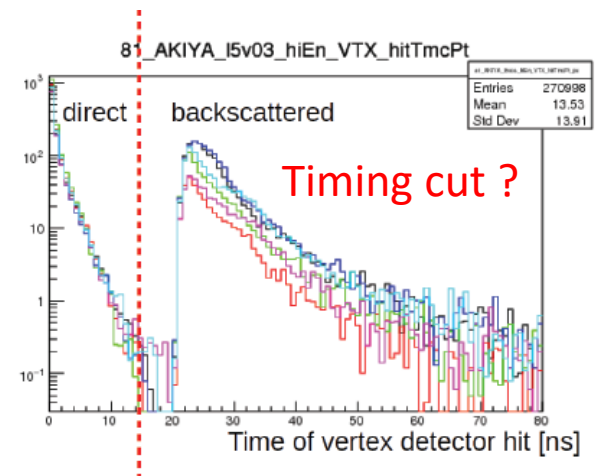
- ✓ achieving simultaneously the ambitioned spatial resolution & material budget is an issue
 - Targeted Mat.Budget ~0.15% X_0 / layer



ILD @ 250 GeV

ILD-I5-v05	hits/BX			hits/BX/cm ²		
	mean	\pm	RMS	mean	\pm	RMS
VXD 1	914	\pm	364	6.64	\pm	2.65
VXD 2	545	\pm	207	3.96	\pm	1.51
VXD 3	129	\pm	60	0.213	\pm	0.100
VXD 4	107	\pm	53	0.177	\pm	0.088
VXD 5	40	\pm	26	0.043	\pm	0.029
VXD 6	34	\pm	24	0.037	\pm	0.026

Daniel Jeans, Akiya Miyamoto

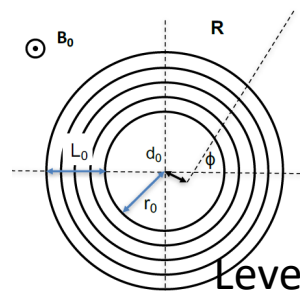


Cut at 15 ns

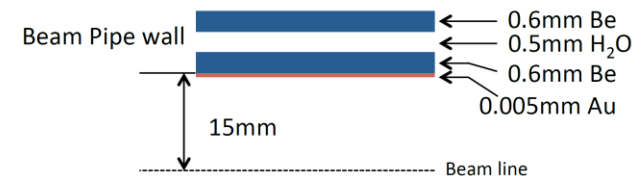
Quest for updates

- **Relevant sources of beam related backgrounds:**
 - ✓ news on beamstrahlung since TDR ? (D.Jeans/A.Miyamoto studies)
 - ✓ other phenomena: synchrotron radiation, infra-red radiation, ...
 - ✓ are there potential transitory backgrounds ?
 - ✓ consequences of potential luminosity upgrade (change in optics ?)
- **Which are the suspected sources of uncertainty** on the prediction of beam related backgrounds:
 - ✓ beamstrahlung generators: γ & e^\pm rates, momentum spectrum
 - ✓ other phenomena: synchrotron radiation, infra-red radiation, ...
 - ✓ corresponding safety factors ? x3-5 ?
- **Beam parameters:**
 - ✓ beamstrahlung envelope at small polar angle prevents reconstruction of shallow tracks close to IP
 - ✓ at SuperKEKb (and FCCee < Sync. radiation) beam pipe cooling is considered as mandatory: Why not at ILC ?
 - Cooling \Rightarrow beam pipe material budget x 2 ($\sim 0.15\%X_0 \Rightarrow 0.3\%X_0$)
 - ✓ At FCCee they consider reducing the inner radius (more material budget, lower magnetic field w.r.t. ILC)
 - Is it worth considering it ?

$$\Delta d_0|_{res.} \approx \frac{3\sigma_{r\phi}}{\sqrt{N+5}} \sqrt{1 + \frac{8r_0}{L_0} + \frac{28r_0^2}{L_0^2} + \frac{40r_0^3}{L_0^3} + \frac{20r_0^4}{L_0^4}}$$

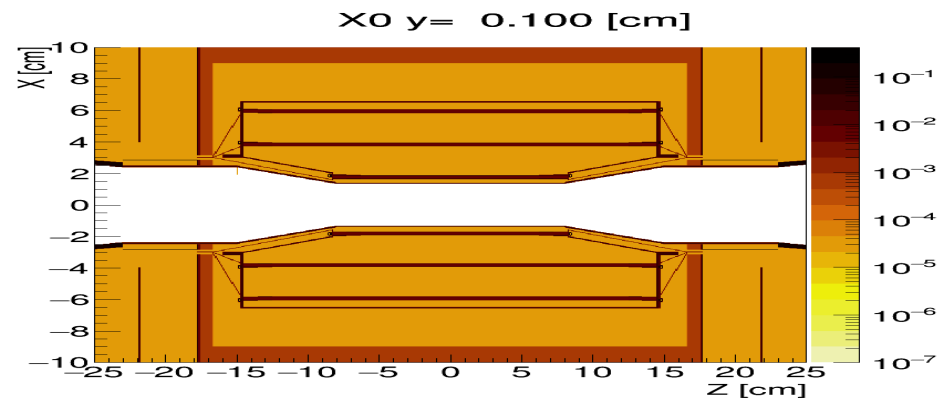
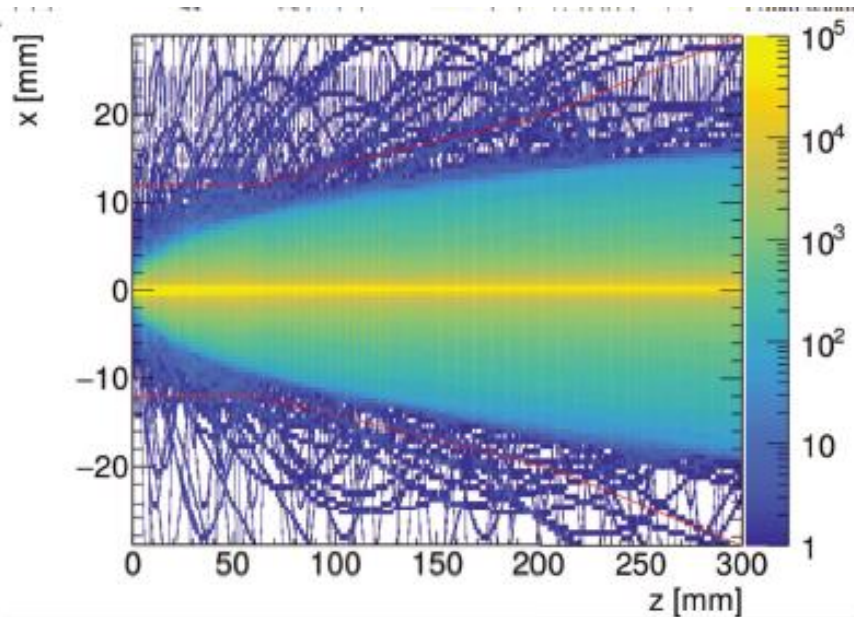


Level arm !



FCCee beampipe

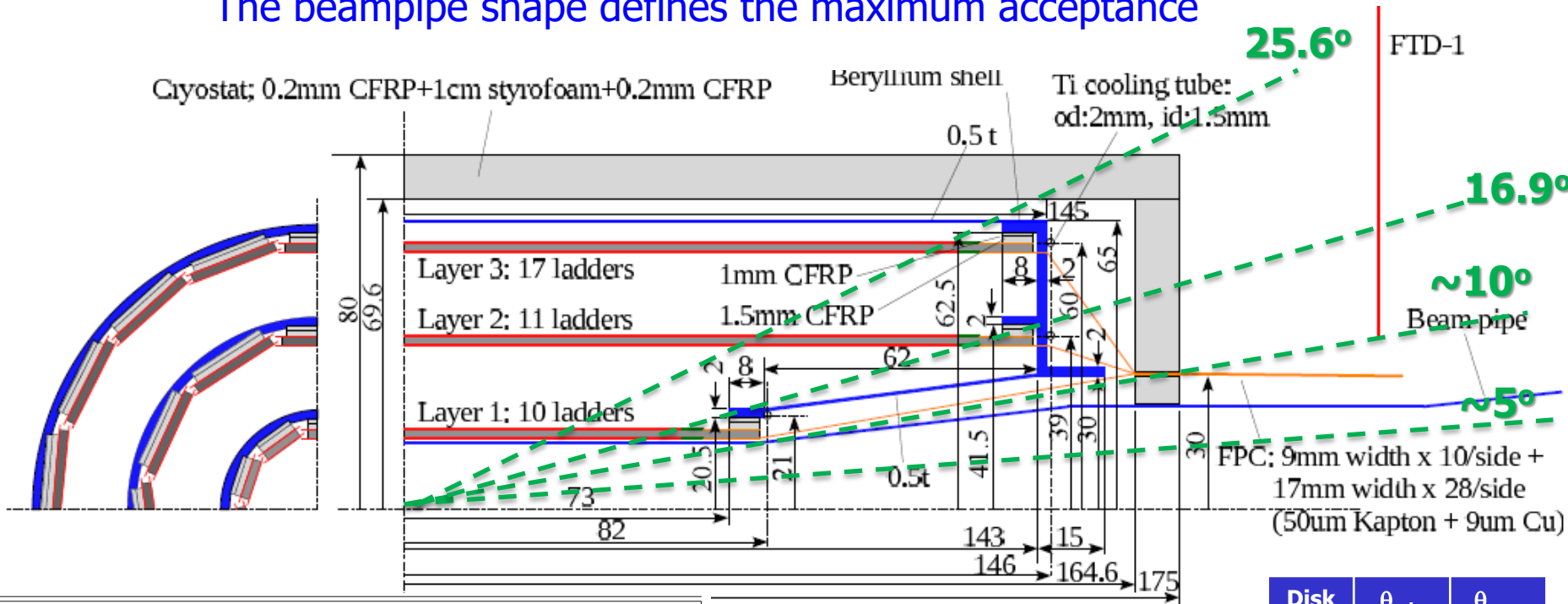
Beam background envelope



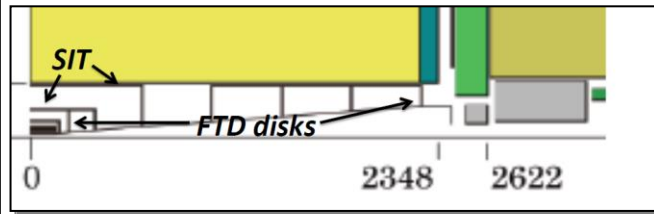
The beam background envelope defines somehow the acceptance limit

Geometry in the forward region (ILD)

The beampipe shape defines the maximum acceptance



FTD characteristics (design baseline: pixels for two inner disks, microstrips for the rest)				
Geometry			Characteristics	Material
R[mm]	Z[mm]	$\cos\theta$	Resolution $R-\phi[\mu\text{m}]$	RL[%]
39-164	220	0.985-0.802	$\sigma=3-6$	0.25-0.5
49.6-164	371.3	0.991-0.914		0.25-0.5
70.1-308	644.9	0.994-0.902		0.65
100.3-309	1046.1	0.994-0.959	$\sigma=7.0$	0.65
130.4-309	1447.3	0.995-0.998		0.65
160.5-309	1848.5	0.996-0.986		0.65
190.5-309	2250	0.996-0.990		0.65

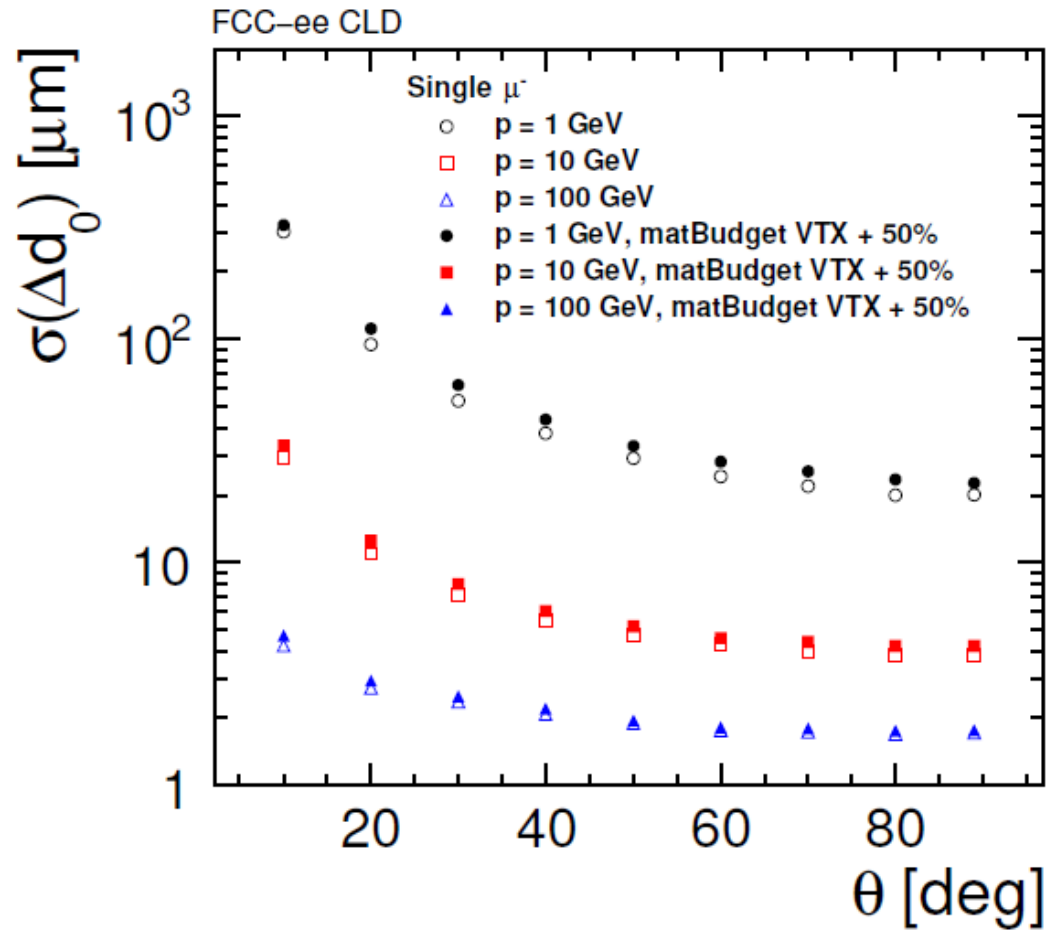


Disk	θ_{\min}	θ_{\max}
1	10.1°	36.7°
2	7.6°	23.8°
3	6.2°	25.5°
4	5.5°	16.5°
5	5.1°	12.1°
6	5.0°	9.5°
7	4.8°	7.8°

Polar θ (°)	>25.5	25.5-17	17-10	10-5
VXD hits	6	4	0	0
FTD hits (disks)	0	2-3 (1-2-3)	4 (1-2-3-4)	4-6 (2 to 7)

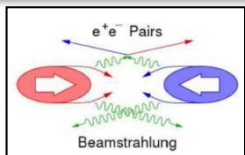
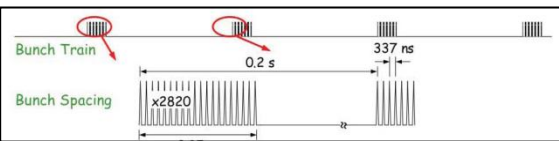
Back up

Material budget: FCCee example



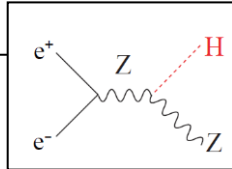
(a) d_0 resolution

ILC/ FCCee Vertex detector requirements

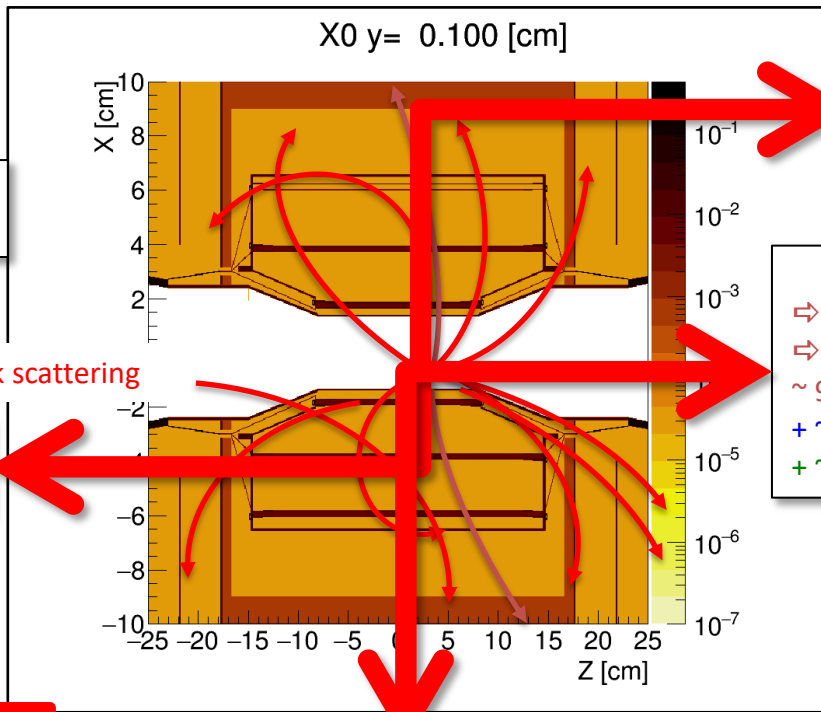


Physics (Hz/cm^2)
 Beam background ($\sim 5 \text{ hits/BX/cm}^2$ on layer 0, ILC)

- Physics
- ⇒ Flavour tagging
 - ⇒ Low p_T tracks
 - ⇒ Vertex/Jet charge determination



Beam background



- Vertex reconstruction
- ⇒ granularity
 - ⇒ Pitch $\sim 17 \mu\text{m}$
 - ⇒ ($\sigma_{\text{sp}} \sim 3 \mu\text{m}$)

Radiation hardness
 $O(100\text{kRad/yr})$ & $O(10^{11})n_{\text{eq}}/\text{yr}$

Rad.Tol. devices

- Material Budget
- ⇒ $\sim 0.15\% X_0$ / layer
 - ⇒ $< 1\% X_0$ for the whole VTX
 - $\sim 900 \mu\text{m Si}$
 - + $\sim 0.14\% X_0$ for the beam pipe (ILC)
 - + $\sim 0.3\% X_0$ for the beam pipe (FCC)

Read-out speed
 $O(1-10 \mu\text{s})$

Low material detectors & supports structures

Power consumption
 $\sim < 50\text{mW/cm}^2$

Fast read-out & low Power Architectures ($\sim 20 \text{ mW/cm}^2$)

Cooling Stiffness / Alignment

$$\sigma_{d_0} = a \oplus \frac{b}{p \sin^{3/2} \theta}$$

$a \simeq 5 \mu\text{m}; \quad b \sim 15 \mu\text{m} \cdot \text{GeV}$ FCCee
 $b \sim 10 \mu\text{m} \cdot \text{GeV}$ ILC

Challenge:

- ⇒ Keep excellent spatial resolution, low material budget, moderate Power consumption and push towards better time resolution (BX)

Spatial resolution in Higgs factories

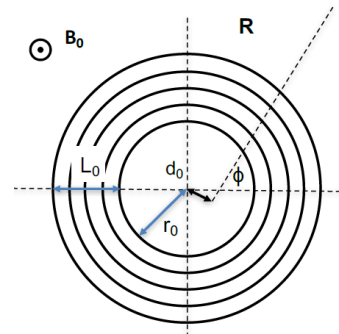
Why do we need vertexing and tracking ?

- ✓ Reconstruct Primary and secondary vertex
 - Heavy flavor tagging (b, c, τ)
 - Order of magnitude: O(1μm) - O(10μm)
- ✓ Low momentum tracking
 - pT ~< 100 MeV/c - 1 GeV/c
- ✓ Vertex/Jet charge determination

$$\Delta d_0|_{res.} \approx \frac{3\sigma_{r\phi}}{\sqrt{N+5}} \sqrt{1 + \frac{8r_0}{L_0} + \frac{28r_0^2}{L_0^2} + \frac{40r_0^3}{L_0^3} + \frac{20r_0^4}{L_0^4}}$$

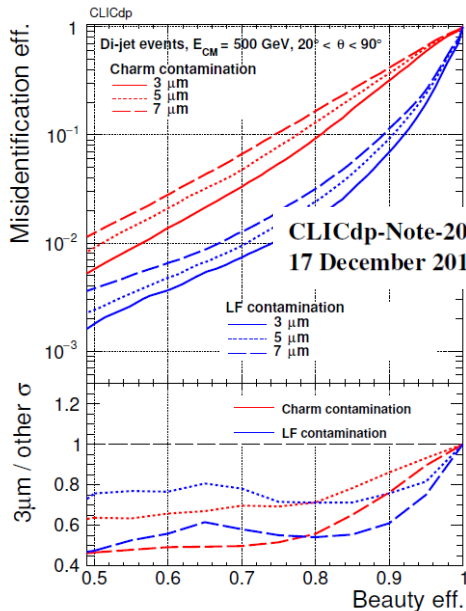
$$\Delta d_0|_{m.s.} \approx \frac{0.0136 \text{ GeV}/c}{\beta p_T} r_0 \sqrt{\frac{d}{X_0 \sin \theta} \sqrt{1 + \frac{1}{2} \left(\frac{r_0}{L_0}\right) + \frac{N}{4} \left(\frac{r_0}{L_0}\right)^2}}$$

$$\sigma_{d_0}^2 = a^2 + \left(\frac{b}{p \cdot \sin^{3/2} \theta} \right)^2$$

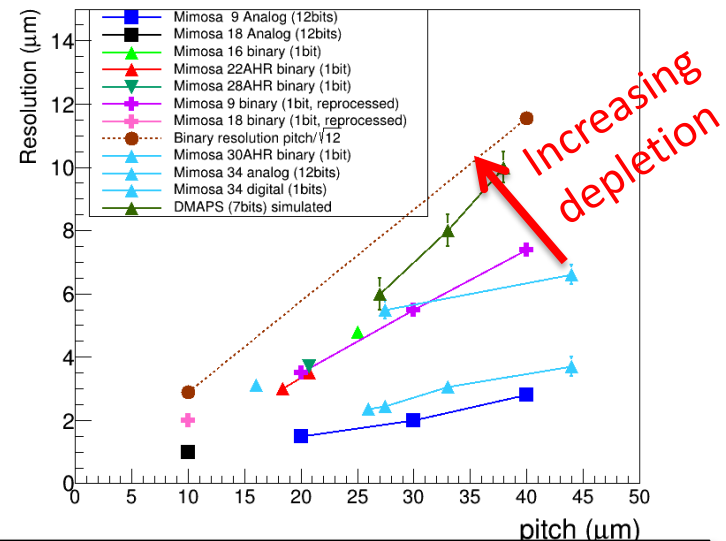
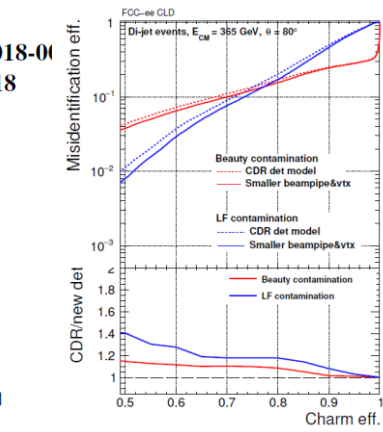


Level arm !

CMOS pixel resolution vs pitch



Sensitivity to b/c-tagging performances



⇒ σ_{sp} ~ 3 μm ⇔ pitch ~ 17 μm
(assuming binary output, ~20 μm epi.thickness & partial depletion in 180nm tech.)

Material budget in Higgs factories

$$\sigma_{d_0}^2 = a^2 + \left(\frac{b}{p \cdot \sin^{3/2} \theta} \right)^2$$

• Driving parameter

✓ Inner radius

$$\Delta d_0|_{m.s.} \approx \frac{0.0136 \text{ GeV}/c}{\beta p_T} r_0 \sqrt{\frac{d}{X_0 \sin \theta} \sqrt{1 + \frac{1}{2} \left(\frac{r_0}{L_0} \right) + \frac{N}{4} \left(\frac{r_0}{L_0} \right)^2}}$$

✓ Beam pipe

▪ Constant term ~ 0.15-0.3 % X_0

✓ Material budget / layer

▪ Requirement ~ ~0.15% X_0 /layer

• Material budget optimization

✓ Double sided approach

▪ PLUME prototypes

✓ Stitching (see later)

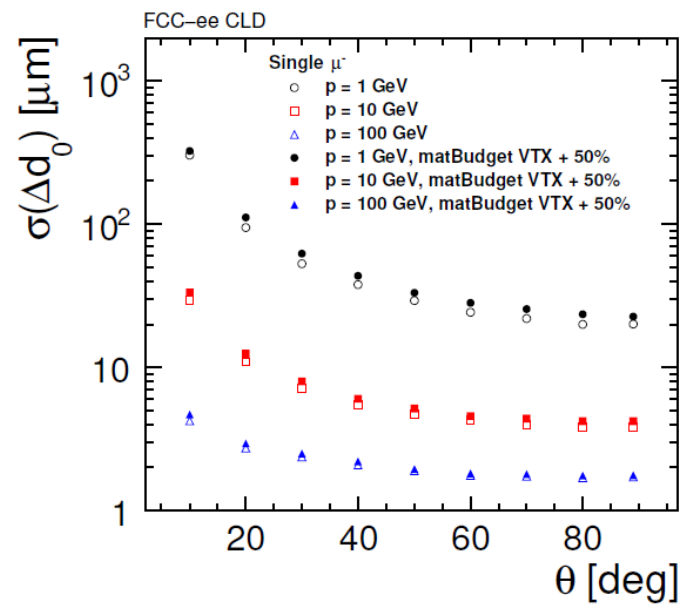
▪ Larger surfaces

✓ Bent sensors (see later)

▪ Optimize

✓ Integration

▪ Cooling system, mech. Support, cabling, Powering scheme, etc.



(a) d_0 resolution

Sensitivity to impact parameter resolution

Power & cooling in Higgs Factories

- Baseline:
 - ✓ air flow cooling only to minimize material budget
 - ✓ Up to ~ 20 mW/cm²
- Driving parameters:
 - ✓ # channels, Time resolution / data flux
 - ✓ Surface (VXD ~ 3500 cm²)
- Power Pulsing (ILC/CLIC)
 - ✓ Constraints more relaxed w.r.t. FCCee

Power Analog (mW/chip)	49.22
Power Bias (mW/chip)	4.5
Power PriorityEncoder (mW/chip)	4.219
Power DigitalPeriphery (mW/chip)	64.27
Power PLL (mW/chip)	18.5
Power Serializer With Data (mW/chip)	86.06
Power Serializer With No Data (mW/chip)	0
Power LVDS (mW/chip)	56.4

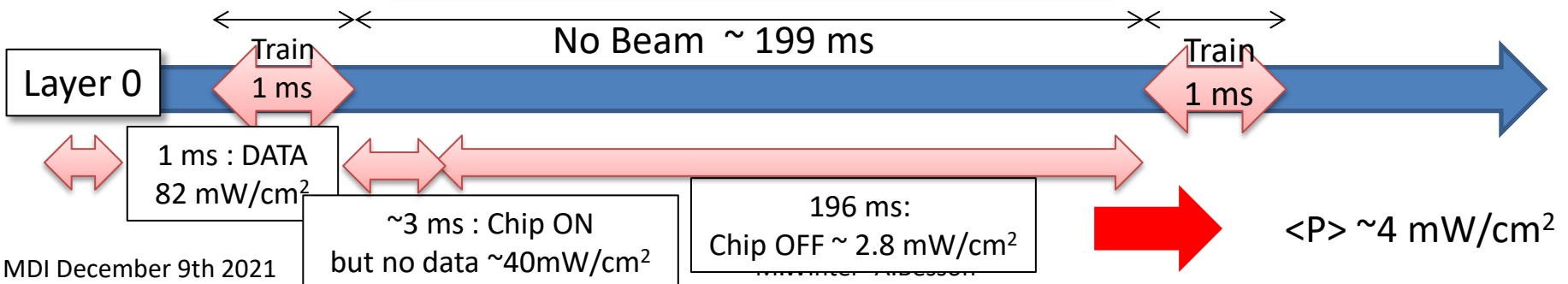
MIMOSIS like architecture, 180 nm

Period	Relative Energy
E during train	225 mJ $\sim 4\%$
E between train (Power ON)	380 mJ $\sim 6\%$
E between train (Power OFF)	5740 mJ $\sim 90\%$

Beam background rate	Read-out speed	<Power (NO P.P.)>	<Power> (P.P.)	
			Conservative	Ambitious
	(μ s)	(W)		
DBD	4 μ s	102 W (~ 30 mW/cm ²)	~ 31 W (~ 10 mW/cm ²)	~ 12 W
DBD	2 μ s	122 W (~ 33 mW/cm ²)		
DBD x 2	4 μ s	107 W		
DBD x 2	2 μ s	127 W		

Layers	Relative Power
Layers 0/1	$\sim 10\%$
Layers 2/3	$\sim 35\%$
Layers 4/5	$\sim 55\%$

Challenge: Air flow cooling only



ILC & FCC differences

- Beam structure: « continuous » vs trains
 - ✓ Power Pulsing: allows a factor $O(10)$ reduction in average power
 - ✓ ILC: However, avoiding PP is desirable (alignment)
- Beam pipe shape and material
 - ✓ ILC: $\sim 0.14\%$ X_0 for the beam pipe (500 μm)
 - ✓ FCCee: Sync. Radiations \Rightarrow Cooling of the beam pipe \Rightarrow higher Mat.Budget
 - \Rightarrow 800 (2 pipes) + 400 (water) \sim 1200 μm Be eq.)
 - \Rightarrow Smaller inner radius @ FCCee ?
- MDI:
 - ✓ CLD: Forward acceptance limited to 150 mradian (8.6°)
 - ✓ ILD: Forward acceptance (disks) $\sim 5^\circ$
- TeraZ vs Giga Z
 - ✓ Specific timing and impact parameter resolution ?
 - e.g. lower radius ?
- Magnetic field:
 - ✓ ILC: 3.5/4 T ($R_{\text{max}} \sim 1.8\text{m}$)
 - ✓ CLIC: R_{max} (CLIC): 1.5m
 - ✓ FCC: 2 T max \Rightarrow compensate by larger level arm ($R_{\text{max}} \sim 2.15\text{m}$)

